

PATENT CFR Leurá 4/20/00

HE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Jiang et al.

Serial No.: 09/483,712

Filed: January 14, 2000

For: METHOD OF FABRICATING CHIP-SCALE PACKAGES AND RESULTING

STRUCTURES

Examiner: Unknown

Group Art Unit: 2811

Attorney Docket No.: 3815US (98-0670)

CERTIFICATE OF MAILING

I hereby certify that this paper or fee along with any attachments referred to or identified as being attached or enclosed is being deposited with the United States Postal Service as First Class Mail (under 37 C.F.R. § 1.8(a)) on the date of deposit shown below with sufficient postage and in an envelope addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231.

April 4, 2000 Date of Deposit

Signature of registered practitioner or other person having reasonable basis to expect mailing to occur on date of deposit shown pursuant to 37 C.F.R. § 1.8(a)(1)(ii)

Joseph A. Walkowski
Typed/printed name of person whose signature is contained above

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REQUEST FOR CORRECTED FILING RECEIPT

Application Processing Division Customer Correction Branch Assistant Commissioner for Patents Washington, D.C. 20231

Sir:

Attached is a copy of the official filing receipt received from the PTO in the above application. Issuance of a corrected filing receipt in accordance with the annotations on the attached filing receipt copy is respectfully requested.

The errors to be corrected are as follows:

In the Applicant(s) Section, before "Edward", insert -- Tongbi Jiang, Boise, ID;--

These corrections are not due to any errors by applicants and no fee is due.

Respectfully submitted,

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Date: April 4, 2000

JAW/dlm

Enclosure: Copy of Official Filing Receipt with annotations correcting errors

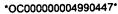
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Trask Britt & Rossa

FILING RECEIPT





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Γ	APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY.DOCKET.NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
L	09/483.712	01/14/2000	2811	852	3815US (98-0670)	4	29	3

Joseph A Walkowski TRASK BRITT & ROSSA P O Box 2550 Salt Lake City, UT 84110



Date Mailed: 03/13/2000

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an rr r is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Customer Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the PTO processes the reply to the Notice, the PTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Edward A. Schrock, Boise, ID;

Tongbi Jiang, Boise, 10; Continuing Data as Claimed by Applicant

Foreign Applications

Foreign filing license granted on 03/13/2000

Title

Method Of Fabricating Chip-Scale Packages And Resulting Structures

Preliminary Class

257

Data entry by: THOMAS, SHEILA

Team: OIPE

Date: 03/13/2000

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